

ARM® Cortex®-M0
32-bit Microcontroller

NuMicro® Family
M071Q/M071V Series BSP
Revision History

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Revision 3.00.003 (Released 2024-03-07)

1. Enable LVR in SYS_PowerDown_MinCurrent sample code to prevent power on/off fail.
2. Use "volatile" with the function pointer to disable compiler optimizations in I2C/UI2C sample code.
3. Add a reaction in TX/RX when encountering a NACK event after ARBLOIF has been triggered in UI2C_Master sample code.
4. Add process when s_Event is SLAVE_SEND_DATA after receiving SLA+R and returned ACK in USCI_I2C_Slave.
5. Remove emWin and ACMP_CTL_NEGSEL_DAC.

Revision 3.00.002 (Released 2023-05-19)

1. Fix sample code build error with compiler 6.
2. Modify SYS_UnLockReg() time-out handler.
3. Call UART FIFO size from uart.h in USB VCOM sample code.
4. Fix USB_MassStorage_CDROM crash on Linux.
5. Fix return in main() cause hardfault issue.
6. Add timeout handler for infinite loop.
7. Add NuMicro.h header.
8. Add SYS_PowerDown_MinCurrent sample code.
9. Add SPI_TRIGGER_TX_RX_PDMA and SPI_DISABLE_TX_RX_PDMA API in SPI driver.
10. Fix SC and EBI sample codes run error.
11. Fix I2C_EEPROM, I2C_GCMode and I2C_Loopback failed in GCC and IAR.

Revision 3.00.001 (Released 2020-07-10)

1. Initial Release

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